

<b>Notice of References Cited</b>	Application/Control No. 09/990,273	Applicant(s)/Patent Under Reexamination HSIAO ET AL.	
	Examiner David S. Kim	Art Unit 2633	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,155,614	10-1992	Carmen et al.	398/202
	B	US-6,617,937 B2	09-2003	Jue et al.	332/109
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"From push-pull circuit to push-pull voltages." IEEE Standard Dictionary of Electrical and Electronics Terms. 3rd ed. 1984.
	V	"Octal bus buffer with 3 state outputs HC 540: inverted - HC 541 non inverted." SGS-Thomson Microelectronics (October 1993): 12 pp. 28 December 2004 < <a href="http://mc.ict.tuwien.ac.at/uebungsteil/projekte/ED1/Logic/74HC541.PDF">http://mc.ict.tuwien.ac.at/uebungsteil/projekte/ED1/Logic/74HC541.PDF</a> >.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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